

Features

- Generates standard call progress tones
- Digital input control
- Linear (analog) output
- Power output capable of driving standard line
- 14-pin DIP and 16-pin SOIC package types
- Single supply 5V CMOS (low power)
- Inexpensive 3.58 MHz time base
- Temperature range from -25°C to 70°C (-01 version)
- Temperature range from -40°C to 85°C (-02 version)

Applications

- Telephone systems
- Test equipment
- Callback
- Security systems
- Billing systems

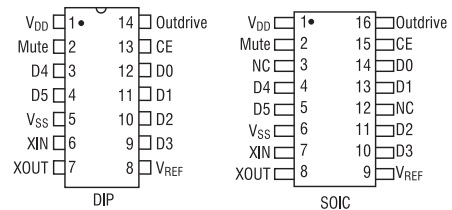
Description

The M-991 is a call progress tone generator integrated circuit for use in telephone systems. The circuit uses low-power CMOS techniques to generate tones which are digitally controlled and highly linear. The M-991 is designed to permit operation with almost any system. The use of integrated circuit techniques allows the M-991 to incorporate the control, tone generating, and power output buffer into a single 14-pin DIP or a 16-pin SOIC. A 3.58-MHz (color burst) crystal-controlled time base guarantees accuracy and repeatability.

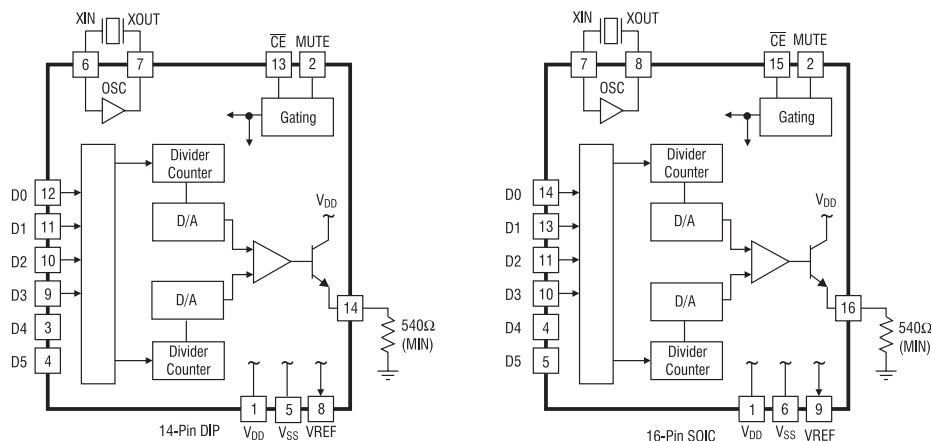
Ordering Information

Part #	Description
M-991	14-pin plastic DIP
M-991-01SM	16-pin SOIC
M-991-01SMTR	16-pin SOIC Tape and Reel
M-991-02SM	16-pin SOIC, Extended Temperature Range
M-991-02SMTR	16-pin SOIC, Extended Temperature Range, Tape and Reel

Pin Assignments



Block Diagram





Absolute Maximum Ratings

Storage Temperature	-55° to 125° C
Operating Ambient Temperature	-25° to 70° C
Operating Ambient Temperature for the M-991-02SM	-40° to 85° C
V _{DD}	7.0V
Any Input Voltage	V _{SS} -0.6 to V _{DD} +0.6V

Note:

- Exceeding these ratings may permanently damage the M-991.

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Specifications

Parameter		Min	Typ	Max	Units	Notes
Power Supply and Reference	V _{DD}	4.75	-	5.25	V	1
	Current Drain, I _{DD}	-	2.0/4.0	-	mA	8
	V _{REF} Pin: Deviation from (V _{DD} + V _{SS})/2	-2	-	+2	%	
	Internal Resistance from V _{REF} to V _{DD} , V _{SS}	3.25	-	6.75	kΩ	
Oscillator	Frequency Deviation	-0.01	-	+0.01	%	7
	External Clock: (XOUT open)					
	V _{IL}	0	-	0.2	V	
	V _{IH}	V _{DD} - 0.2	-	V _{DD}	V	
	Duty Cycle	40	-	60	%	
	XIN, XOUT Loading:					
	Capacitance	-	-	10	pF	10
	Resistance	20	-	-	MΩ	-
Tone Output	Frequency Deviation	-0.5	-	+0.5	%	-
	Level	100	-	180	mV	2
	Distorting Components	-35	-	-	dB	3
	Idle	-	-	-60	dBm	4
	OUTDRIVE Envelope Rise Time	-	-	4	ms	5
Control	DX, CE Pns:					
	V _{IL}	-	-	0.5	V	6
	V _{IH}	2.5	-	-	V	
	Mute Pins:					
	V _{OL} (I _{SINK} = -100 μA)	-	-	1.5	V	
	V _{OH} (I _{SOURCE} = 100 μA)	V _{DD} - 1.5	-	-	V	
Timing	Data Setup (t _{DS})	200	-	-	ns	11
	Data Hold (t _{DH})	10	-	-	ns	
	Chip Enable Fall (t _{PL})	-	-	90	ns	
	Tone On Delay (t _{TO})	-	-	5	ms	
	Tone Off Delay (t _{TD})	-	-	5	ms	
	Mute Delay from Outdrive (t _{MO})	-	-	200	ns	

Notes: (unless otherwise specified)

- All DC voltages are referenced to V_{SS}.
- Vrms per tone, 540 W load.
- Any one frequency relative to the lowest level output tone (f<4000 Hz).
- 0 dBm = 0.775 Vrms.
- To 90% maximum amplitude.
- For all supply voltages in the operating range.

- At XOUT pin as compared to 3.579545 MHz.
- OUTDRIVE with load >5 KW/OUTDRIVE with 540 W load.
- Resistance at V_{REF} to V_{DD} or V_{SS} > 1 MW.
- Crystal oscillator active.
- Measured 90% to 10%.

Call Progress Tone Generation

Call progress tones are audible tones sent from switching systems to calling parties (or equipment) to indicate the status of calls. Calling parties can identify the success of a placed call by what is heard after dialing. The M-991 series utilizes a highly linear tone generator that produces the unique frequencies (singly or in pairs) that are common to call progress signals.

Duration and frequency selection are digitally controlled (see the Data/Tone Selection table below for data settings for a particular tone output). A typical control sequence for the M-991 is: (1) set data lines to desired frequency selection, (2) wait for data lines to settle, (3) drive the chip enable (CE) low, (4) maintain CE low for desired tone duration (Note: data lines may be changed after data hold time), and (4) return CE to

a logic high. (Commonly used call progress tones are shown in the Data/Tone Selection table below.) In a bus-oriented system, noise on the data lines may propagate through the device and appear at the output. To safeguard against this, use an external latch to lock the data into the device. In addition, it is good practice to bypass the V_{REF} pin to ground with a small capacitor (0.01 mF) to reduce power supply noise. The designer should be aware of device timing requirements and design accordingly. The data input pins may be tied high (+5 VDC) or low (ground) as required, but D4 and D5 must be left open. Beware of hardwiring the CE pin for dedicated tone generation. This input is edge triggered. An RC network like that shown in the Power-on Reset Circuit on Page 4 should be used to momentarily reset the device immediately following power-up to ensure proper operation.

Data/Tone Selection

D0	D1	D2	D3	Frequency (Hz)		Use
				1	2	
0	0	0	0	350	440	Dial Tone
0	0	0	1	400	off	Special
0	0	1	0	440	off	Alert Tone
0	0	1	1	440	480	Audible Ring
0	1	0	0	440	620	Pre-empt
0	1	0	1	480	off	Bell high tone
0	1	1	0	480	620	Reorder(Bell low)
0	1	1	1	350	off	Special
1	0	0	0	620	off	Special
1	0	0	1	941	1209	DTMF “*”

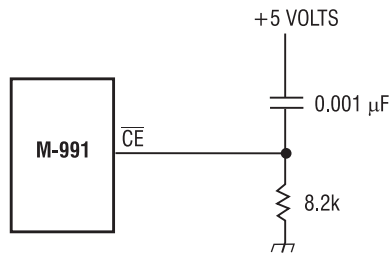
Pin Function

Pin	Function
CE	Latches data and enables output (active low input).
D0 - D3	Data input pins. (See Data/Tone Selection.)
D4-D5	Leave open.
MUTE	Output indicates that a signal is being generated at OUTDRIVE.
OUTDRIVE	Linear buffered tone output.
V_{DD}	Most positive power supply input pin.
V_{REF}	Internally generated mid-power supply voltage (output).
V_{SS}	Most negative power supply input pin.
X_{IN}	Crystal oscillator or digital clock input.
X_{OUT}	Crystal oscillator output.

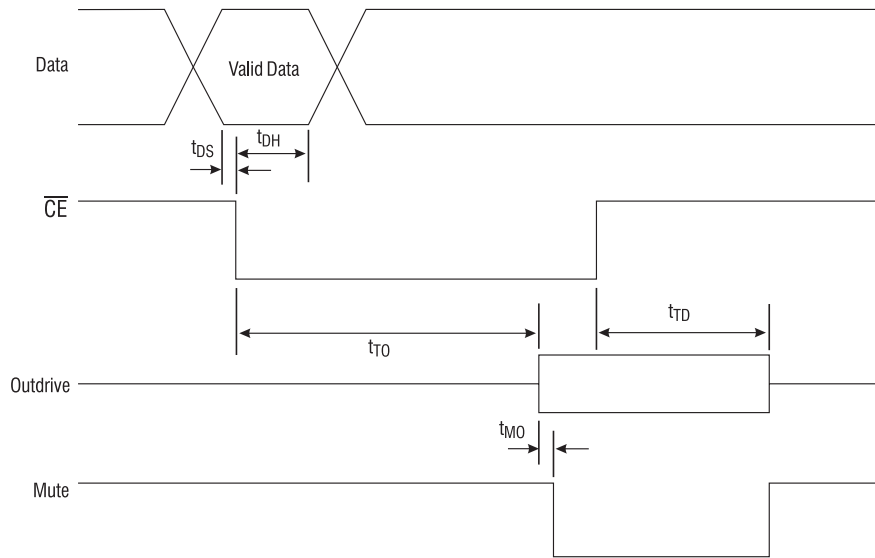
Standard Call Progress Tones

Tone Name	Frequency (Hz)		Interruption Rate
	1	2	
Dial	350	440	Steady
Reorder	480	620	Repeat, tones on and off 250 ms \pm 25 ms each.
Busy	480	620	Repeat, tones on and off 500 ms \pm 50 ms each.
Audible Ring	440	480	Repeat, tones on 2 \pm 0.2 s, tones off 4 \pm 0.4 s
Recall Dial	350	440	Three bursts tones on and off 100 ms \pm 20 ms each followed by dial tone.
Special AR	440	480	Tones on 1 \pm 0.2s, followed by single 440 Hz on for 0.2s on, and silence for 3 \pm 0.3 s, repeat.
Intercept	440	620	Repeat alternating tones, each on for 230 ms \pm 70 ms with total cycle of 500 \pm 50 ms.
Call Waiting	440	Off	One burst 200 \pm 100 ms
Busy Verification	440	Off	One burst of tone on 1.75 \pm 0.25 s before attendant intrudes, followed by burst of tone 0.65 \pm 0.15 s on, 8 to 20 s apart for as long as the call lasts
Executive Override	440	Off	One burst of tone for 3 \pm 1 s before overriding station intrudes
Confirmation	350	440	Three bursts on and off 100 ms each or 100 ms on, 100 ms off, 300 ms on

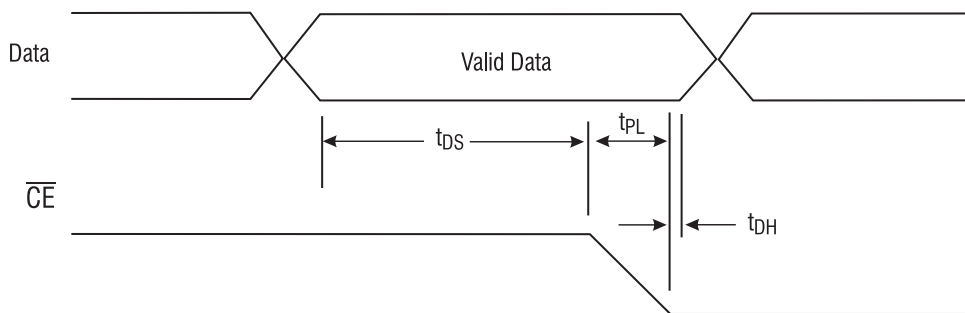
Power-on Reset Circuit



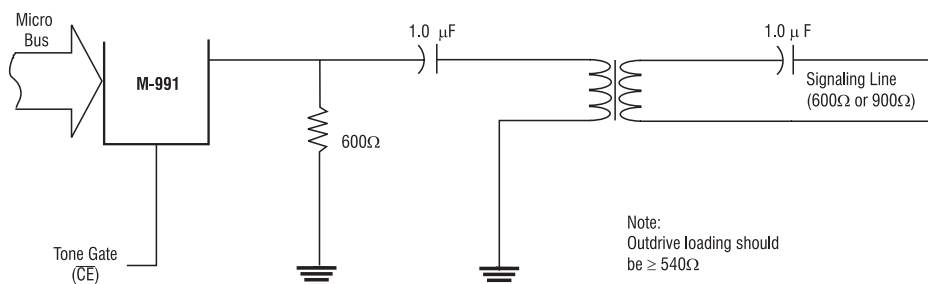
Timing Diagram



Expanded Timing Diagram

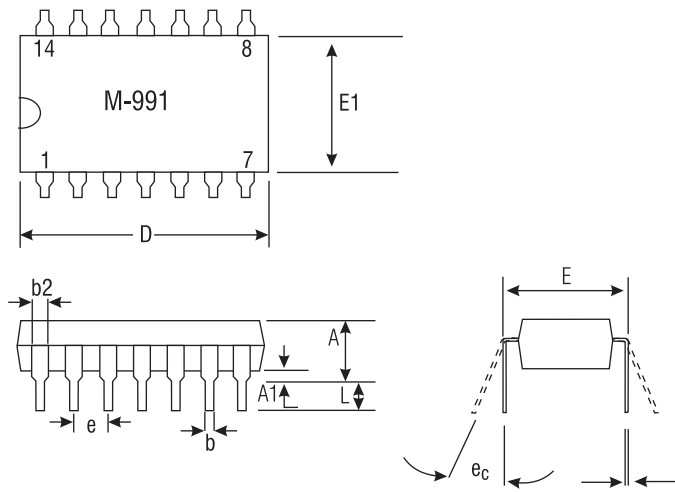


Typical Application



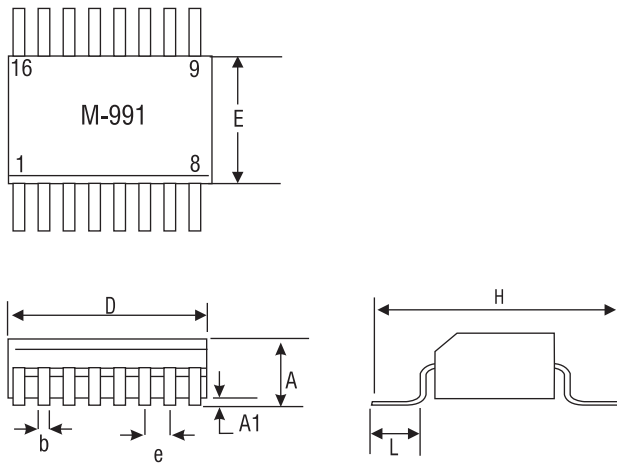
Mechanical Dimensions

14-Pin DIP



	Tolerances			
	Inches		Metric (mm)	
	Min	Max	Min	Max
A	-	.210	-	5.33
A1	.015	-	.38	-
b	.014	.022	.36	.56
b2	.045	.070	1.1	1.8
C	.008	.014	.20	.36
D	.735	.775	18.7	19.7
E	.300	.325	7.6	8.3
E1	.240	.280	6.1	7.1
e	.100 BSC		2.54 BSC	
ec	0°	15°	0°	15°
L	.115	.150	2.9	4.1

16-Pin SOIC



	Tolerances			
	Inches		Metric (mm)	
	Min	Max	Min	Max
A	.0926	.1043	2.35	2.65
A1	.0040	.0118	.10	.30
b	.013	.020	.33	.51
D	.3977	.4133	10.10	10.50
E	.2914	.2992	7.4	7.6
e	.050 BSC		1.27 BSC	
H	.394	.419	10.00	10.65
L	.016	.050	.40	1.27

Drawing not to scale.
Does not reflect actual part marking.

Dimensions
mm
(inches)



CLARE LOCATIONS

Clare Headquarters
78 Cherry Hill Drive
Beverly, MA 01915
Tel: 1-978-524-6700
Fax: 1-978-524-4900
Toll Free: 1-800-27-CLARE

Clare Switch Division
4315 N. Earth City Expressway
Earth City, MO 63045
Tel: 1-314-770-1832
Fax: 1-314-770-1812

Clare Micronix Division
145 Columbia
Aliso Viejo, CA 92656-1490
Tel: 1-949-831-4622
Fax: 1-949-831-4628

SALES OFFICES

AMERICAS

Americas Headquarters

Clare
78 Cherry Hill Drive
Beverly, MA 01915
Tel: 1-978-524-6700
Fax: 1-978-524-4900
Toll Free: 1-800-27-CLARE

Eastern Region

Clare
603 Apache Court
Mahwah, NJ 07430
Tel: 1-201-236-0101
Fax: 1-201-236-8685
Toll Free: 1-800-27-CLARE

Central Region

Clare Canada Ltd.
3425 Harvester Road, Suite 202
Burlington, Ontario L7N 3N1
Tel: 1-905-333-9066
Fax: 1-905-333-1824

Western Region

Clare
1852 West 11th Street, #348
Tracy, CA 95376
Tel: 1-209-832-4367
Fax: 1-209-832-4732
Toll Free: 1-800-27-CLARE

Canada

Clare Canada Ltd.
3425 Harvester Road, Suite 202
Burlington, Ontario L7N 3N1
Tel: 1-905-333-9066
Fax: 1-905-333-1824

EUROPE

European Headquarters

CP Clare nv
Bampsiaan 17
B-3500 Hasselt (Belgium)
Tel: 32-11-300868
Fax: 32-11-300890

France

Clare France Sales
Lead Rep
99 route de Versailles
91160 Champlan
France
Tel: 33 1 69 79 93 50
Fax: 33 1 69 79 93 59

Germany

Clare Germany Sales
ActiveComp Electronic GmbH
Mitterstrasse 12
85077 Manching
Germany
Tel: 49 8459 3214 10
Fax: 49 8459 3214 29

Italy

C.L.A.R.E.s.a.s.
Via C. Colombo 10/A
I-20066 Melzo (Milano)
Tel: 39-02-95737160
Fax: 39-02-95738829

Sweden

Clare Sales
Comptronic AB
Box 167
S-16329 Spånga
Tel: 46-862-10370
Fax: 46-862-10371

United Kingdom

Clare UK Sales
Marco Polo House
Cook Way
Bindon Road
Taunton
UK-Somerset TA2 6BG
Tel: 44-1-823 352541
Fax: 44-1-823 352797

ASIA/PACIFIC

Asian Headquarters

Clare
Room N1016, Chia-Hsin, Bldg II,
10F, No. 96, Sec. 2
Chung Shan North Road
Taipei, Taiwan R.O.C.
Tel: 886-2-2523-6368
Fax: 886-2-2523-6369

<http://www.clare.com>

Clare, Inc. makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.

Specification: DS-M991-R1
©Copyright 2001, Clare, Inc.
All rights reserved. Printed in USA.
7/26/01