## Features

- $600 \mathrm{MHz}-3 \mathrm{~dB}$ bandwidth
- 6 mA supply current (per amplifier)
- Single and dual supply operation, from 5 V to 10 V
- Available in 16-pin QSOP package
- Single (EL5192C) and Dual (EL5292C) available
- High speed, 1 GHz product available (EL5191C)
- Low power, $4 \mathrm{~mA}, 300 \mathrm{MHz}$ product available (EL5193C, EL5293C, and EL5393C


## Applications

- Video Amplifiers
- Cable Drivers
- RGB Amplifiers
- Test Equipment
- Instrumentation
- Current to Voltage Converters


## Ordering Information

| Part No | Package |  <br> Reel | Outline \# |
| :--- | :---: | :---: | :---: |
| EL5392CS | 16-Pin SO | - | MDP0027 |
| EL5392CS-T7 | 16-Pin SO | $7 "$ | MDP0027 |
| EL5392CS-T13 | 16-Pin SO | $13 "$ | MDP0027 |
| EL5392CU | 16-Pin QSOP | - | MDP0040 |
| EL5392CU-T13 | 16-Pin QSOP | $13 "$ | MDP0040 |

## General Description

The EL5392C is a triple current feedback amplifier with a very high bandwidth of 600 MHz . This makes this amplifier ideal for today's high speed video and monitor applications.

With a supply current of just 6 mA per amplifier and the ability to run from a single supply voltage from 5 V to 10 V , the EL5392C is also ideal for hand held, portable or battery powered equipment.

For applications where board space is critical, the EL5392C is offered in the 16-pin QSOP package, as well as an industry standard 16-pin SO. The EL5392C operates over the industrial temperature range of $40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$.

## Pin Configurations



## EL5392C

Triple 600 MHz Current Feedback Amplifier

## Absolute Maximum Ratings $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right)$

Values beyond absolute maximum ratings can cause the device to be prematurely damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
Supply Voltage between $\mathrm{V}_{\mathrm{S}}+$ and $\mathrm{V}_{\mathrm{S}}$
Maximum Continuous Output Current

## Important Note:

All parameters having Min/Max specifications are guaranteed. Typ values are for information purposes only. Unless otherwise noted, all tests are at the specified temperature and are pulsed tests, therefore: $\mathbf{T}_{J}=\mathbf{T}_{\mathbf{C}}=\mathbf{T}_{\mathbf{A}}$.

## Electrical Characteristics

$V_{S^{+}}=+5 V, V_{S^{-}}=-5 V, R_{F}=750 \Omega$ for $A_{V}=1, R_{F}=375 \Omega$ for $A_{V}=2, R_{L}=150 \Omega, T_{A}=25^{\circ} C$ unless otherwise specified.

| Parameter | Description | Conditions | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| AC Performance |  |  |  |  |  |  |
| BW | -3dB Bandwidth | $\mathrm{A}_{\mathrm{V}}=+1$ |  | 600 |  | MHz |
|  |  | $\mathrm{A}_{\mathrm{V}}=+2$ |  | 300 |  | MHz |
| BW1 | 0.1dB Bandwidth |  |  | 25 |  | MHz |
| SR | Slew Rate | $\mathrm{V}_{\mathrm{O}}=-2.5 \mathrm{~V}$ to $+2.5 \mathrm{~V}, \mathrm{~A}_{\mathrm{V}}=+2$ | 2100 | 2300 |  | V/us |
| ts | 0.1\% Settling Time | $\mathrm{V}_{\text {OUT }}=-2.5 \mathrm{~V}$ to $+2.5 \mathrm{~V}, \mathrm{Av}=-1$ |  | 9 |  | ns |
| $\mathrm{C}_{S}$ | Channel Separation | $\mathrm{f}=5 \mathrm{MHz}$ |  | 60 |  | dB |
| $\mathrm{e}_{\mathrm{n}}$ | Input Voltage Noise |  |  | 4.1 |  | $\mathrm{nV} / \sqrt{ } \mathrm{Hz}$ |
| $\mathrm{in}^{-}$ | IN- input current noise |  |  | 20 |  | $\mathrm{pA} / \sqrt{ } \mathrm{Hz}$ |
| $\mathrm{i}_{\mathrm{n}}+$ | IN+ input current noise |  |  | 50 |  | $\mathrm{pA} / \sqrt{ } \mathrm{Hz}$ |
| dG | Differential Gain Error ${ }^{[1]}$ | $\mathrm{A}_{\mathrm{V}}=+2$ |  | 0.015 |  | \% |
| dP | Differential Phase Error ${ }^{[1]}$ | $\mathrm{A}_{\mathrm{V}}=+2$ |  | 0.04 |  | - |
| DC Performance |  |  |  |  |  |  |
| $\mathrm{V}_{\text {OS }}$ | Offset Voltage |  | -10 | 1 | 10 | mV |
| $\mathrm{T}_{\mathrm{C}} \mathrm{V}_{\text {OS }}$ | Input Offset Voltage Temperature Coefficient | Measured from $\mathrm{T}_{\text {MIN }}$ to $\mathrm{T}_{\text {MAX }}$ |  | 5 |  | $\mu \mathrm{V} /{ }^{\circ} \mathrm{C}$ |
| $\mathrm{R}_{\mathrm{OL}}$ | Transimpediance |  | 200 | 400 |  | $\mathrm{k} \Omega$ |
| Input Characteristics |  |  |  |  |  |  |
| CMIR | Common Mode Input Range |  | $\pm 3$ | $\pm 3.3$ |  | V |
| CMRR | Common Mode Rejection Ratio |  | 42 | 50 |  | dB |
| $+\mathrm{I}_{\text {IN }}$ | + Input Current |  | -60 | 3 | 60 | $\mu \mathrm{A}$ |
| $-\mathrm{I}_{\text {IN }}$ | - Input Current |  | -40 | 4 | 40 | $\mu \mathrm{A}$ |
| $\mathrm{R}_{\text {IN }}$ | Input Resistance |  |  | 37 |  | $\mathrm{k} \Omega$ |
| $\mathrm{C}_{\text {IN }}$ | Input Capacitance |  |  | 0.5 |  | pF |
| Output Characteristics |  |  |  |  |  |  |
| $\mathrm{V}_{\mathrm{O}}$ | Output Voltage Swing | $\mathrm{R}_{\mathrm{L}}=150 \Omega$ to GND | $\pm 3.4$ | $\pm 3.7$ |  | V |
|  |  | $\mathrm{R}_{\mathrm{L}}=1 \mathrm{k} \Omega$ to GND | $\pm 3.8$ | $\pm 4.0$ |  | V |
| I ${ }_{\text {OUT }}$ | Output Current | $\mathrm{R}_{\mathrm{L}}=10 \Omega$ to GND | 95 | 120 |  | mA |
| Supply |  |  |  |  |  |  |
| Is ${ }_{\text {ON }}$ | Supply Current | No Load, $\mathrm{V}_{\text {IN }}=0 \mathrm{~V}$ | 5 | 6 | 7.25 | mA |
| PSRR | Power Supply Rejection Ratio | DC, $\mathrm{V}_{\mathrm{S}}= \pm 4.75 \mathrm{~V}$ to $\pm 5.25 \mathrm{~V}$ | 55 | 75 |  | dB |
| -IPSR | - Input Current Power Supply Rejection | DC, $\mathrm{V}_{\mathrm{S}}= \pm 4.75 \mathrm{~V}$ to $\pm 5.25 \mathrm{~V}$ | -2 |  | 2 | $\mu \mathrm{A} / \mathrm{V}$ |

1. Standard NTSC test, AC signal amplitude $=286 \mathrm{mV}_{\mathrm{P}-\mathrm{P}}, \mathrm{f}=3.58 \mathrm{MHz}$

## Typical Performance Curves



## EL5392C <br> Triple 600MHz Current Feedback Amplifier

## Typical Performance Curves

Frequency Response for Various $C_{L}$


Group Delay vs Frequency


Transimpedance (ROL) vs Frequency


Frequency Response for Various $\mathrm{R}_{\mathrm{F}}$


Frequency Response for Various Common-mode Input Voltages


PSRR and CMRR vs Frequency


## Typical Performance Curves



Peaking vs Supply Voltage for Non-inverting Gains

-3dB Bandwidth vs Temperature for Non-inverting Gains

-3dB Bandwidth vs Supply Voltage for Inverting Gains


Peaking vs Supply Voltage for Inverting Gains

-3dB Bandwidth vs Temperature for Inverting Gains


## EL5392C <br> Triple 600 MHz Current Feedback Amplifier

## Typical Performance Curves



Closed Loop Output Impedance vs Frequency


2nd and 3rd Harmonic Distortion vs Frequency



Supply Current vs Supply Voltage



## Typical Performance Curves




Small Signal Step Response


10ns/div

Differential Gain/Phase vs DC Input Voltage at 3.58 MHz


Output Voltage Swing vs Frequency THD<0.1\%


Large Signal Step Response

$10 \mathrm{~ns} / \mathrm{div}$

## Typical Performance Curves

Settling Time vs Settling Accuracy


PSRR and CMRR vs Temperature


Offset Voltage vs Temperature


Transimpedance (Rol) vs Temperature


ICMR and IPSR vs Temperature


## Input Current vs Temperature



## Typical Performance Curves



Positive Output Swing vs Temperature for Various Loads


Output Current vs Temperature


Supply Current vs Temperature



Slew Rate vs Temperature


## Typical Performance Curves




## Pin Descriptions

| $\begin{aligned} & \text { EL5392C } \\ & \text { 16-Pin SO } \end{aligned}$ | $\begin{gathered} \text { EL5392C } \\ \text { 16-Pin QSOP } \end{gathered}$ | Pin Name | Function | Equivalent Circuit |
| :---: | :---: | :---: | :---: | :---: |
| 1 | 1 | INA+ | Non-inverting input, channel A |  |
| 2, 4, 7 | 2, 4, 7 | NC | Not connected (leave disconnected) |  |
| 3 | 3 | $\mathrm{V}_{\text {S }}$ | Negative supply |  |
| 5 | 5 | INB+ | Non-inverting input, channel B | (See circuit 1) |
| 6,11 | 6,11 | NC | Not connected |  |
| 8 | 8 | INC+ | Non-inverting input, channel C | (See circuit 1) |
| 9 | 9 | INC- | Inverting input, channel C | (See circuit 1) |
| 10 | 10 | OUTC | Output, channel C |  |
| 12 | 12 | INB- | Inverting input, channel B | (See circuit 1) |
| 13 | 13 | OUTB | Output, channel B | (See circuit 2) |
| 14 | 14 | $\mathrm{V}^{+}+$ | Positive supply |  |
| 15 | 15 | OUTA | Output, channel A | (See circuit 2) |
| 16 | 16 | INA- | Inverting input, channel A | (See circuit 1) |

## Applications Information

## Product Description

The EL5392C is a current-feedback operational amplifier that offers a wide -3 dB bandwidth of 600 MHz and a low supply current of 6 mA per amplifier. The EL5392C works with supply voltages ranging from a single 5 V to 10 V and they are also capable of swinging to within 1 V of either supply on the output. Because of their currentfeedback topology, the EL5392C does not have the normal gain-bandwidth product associated with voltagefeedback operational amplifiers. Instead, its -3 dB bandwidth to remain relatively constant as closed-loop gain is increased. This combination of high bandwidth and low power, together with aggressive pricing make the EL5392C the ideal choice for many low-power/highbandwidth applications such as portable, handheld, or battery-powered equipment.
For varying bandwidth needs, consider the EL5191C with 1 GHz on a 9 mA supply current or the EL5193C with 300 MHz on a 4 mA supply current. Versions include single, dual, and triple amp packages with 5-pin SOT23, 16-pin QSOP, and 8-pin or 16-pin SO outlines.

## Power Supply Bypassing and Printed Circuit Board Layout

As with any high frequency device, good printed circuit board layout is necessary for optimum performance. Low impedance ground plane construction is essential. Surface mount components are recommended, but if leaded components are used, lead lengths should be as short as possible. The power supply pins must be well bypassed to reduce the risk of oscillation. The combination of a $4.7 \mu \mathrm{~F}$ tantalum capacitor in parallel with a $0.01 \mu \mathrm{~F}$ capacitor has been shown to work well when placed at each supply pin.

For good AC performance, parasitic capacitance should be kept to a minimum, especially at the inverting input. (See the Capacitance at the Inverting Input section) Even when ground plane construction is used, it should be removed from the area near the inverting input to minimize any stray capacitance at that node. Carbon or Metal-Film resistors are acceptable with the Metal-Film resistors giving slightly less peaking and bandwidth because of additional series inductance. Use of sockets,
particularly for the SO package, should be avoided if possible. Sockets add parasitic inductance and capacitance which will result in additional peaking and overshoot.

## Capacitance at the Inverting Input

Any manufacturer's high-speed voltage- or currentfeedback amplifier can be affected by stray capacitance at the inverting input. For inverting gains, this parasitic capacitance has little effect because the inverting input is a virtual ground, but for non-inverting gains, this capacitance (in conjunction with the feedback and gain resistors) creates a pole in the feedback path of the amplifier. This pole, if low enough in frequency, has the same destabilizing effect as a zero in the forward openloop response. The use of large-value feedback and gain resistors exacerbates the problem by further lowering the pole frequency (increasing the possibility of oscillation.)

The EL5392C has been optimized with a $375 \Omega$ feedback resistor. With the high bandwidth of these amplifiers, these resistor values might cause stability problems when combined with parasitic capacitance, thus ground plane is not recommended around the inverting input pin of the amplifier.

## Feedback Resistor Values

The EL5392C has been designed and specified at a gain of +2 with $R_{F}$ approximately $375 \Omega$. This value of feedback resistor gives 300 MHz of -3 dB bandwidth at $\mathrm{A}_{\mathrm{V}}=2$ with 2 dB of peaking. With $\mathrm{A}_{V}=-2$, an $\mathrm{R}_{\mathrm{F}}$ of $375 \Omega$ gives 275 MHz of bandwidth with 1 dB of peaking. Since the EL5392C is a current-feedback amplifier, it is also possible to change the value of $\mathrm{R}_{\mathrm{F}}$ to get more bandwidth. As seen in the curve of Frequency Response for Various $\mathrm{R}_{\mathrm{F}}$ and $\mathrm{R}_{\mathrm{G}}$, bandwidth and peaking can be easily modified by varying the value of the feedback resistor.
Because the EL5392C is a current-feedback amplifier, its gain-bandwidth product is not a constant for different closed-loop gains. This feature actually allows the EL5392C to maintain about the same -3dB bandwidth. As gain is increased, bandwidth decreases slightly while stability increases. Since the loop stability is improving
with higher closed-loop gains, it becomes possible to reduce the value of $R_{F}$ below the specified $375 \Omega$ and still retain stability, resulting in only a slight loss of bandwidth with increased closed-loop gain.

## Supply Voltage Range and Single-Supply Operation

The EL5392C has been designed to operate with supply voltages having a span of greater than 5 V and less than 10 V . In practical terms, this means that the EL5392C will operate on dual supplies ranging from $\pm 2.5 \mathrm{~V}$ to $\pm 5 \mathrm{~V}$. With single-supply, the EL5392C will operate from 5 V to 10 V .

As supply voltages continue to decrease, it becomes necessary to provide input and output voltage ranges that can get as close as possible to the supply voltages. The EL5392C has an input range which extends to within 2V of either supply. So, for example, on $\pm 5 \mathrm{~V}$ supplies, the EL5392C has an input range which spans $\pm 3 \mathrm{~V}$. The output range of the EL5392C is also quite large, extending to within 1 V of the supply rail. On a $\pm 5 \mathrm{~V}$ supply, the output is therefore capable of swinging from -4 V to +4 V . Single-supply output range is larger because of the increased negative swing due to the external pull-down resistor to ground.

## Video Performance

For good video performance, an amplifier is required to maintain the same output impedance and the same frequency response as DC levels are changed at the output. This is especially difficult when driving a standard video load of $150 \Omega$, because of the change in output current with DC level. Previously, good differential gain could only be achieved by running high idle currents through the output transistors (to reduce variations in output impedance.) These currents were typically comparable to the entire 6 mA supply current of each EL5392C amplifier. Special circuitry has been incorporated in the EL5392C to reduce the variation of output impedance with current output. This results in dG and dP specifications of $0.015 \%$ and $0.04^{\circ}$, while driving $150 \Omega$ at a gain of 2 .
Video performance has also been measured with a $500 \Omega$ load at a gain of +1 . Under these conditions, the

EL5392C has dG and dP specifications of $0.03 \%$ and $0.05^{\circ}$, respectively.

## Output Drive Capability

In spite of its low 6 mA of supply current, the EL5392C is capable of providing a minimum of $\pm 95 \mathrm{~mA}$ of output current. With a minimum of $\pm 95 \mathrm{~mA}$ of output drive, the EL5392C is capable of driving $50 \Omega$ loads to both rails, making it an excellent choice for driving isolation transformers in telecommunications applications.

## Driving Cables and Capacitive Loads

When used as a cable driver, double termination is always recommended for reflection-free performance. For those applications, the back-termination series resistor will decouple the EL5392C from the cable and allow extensive capacitive drive. However, other applications may have high capacitive loads without a back-termination resistor. In these applications, a small series resistor (usually between $5 \Omega$ and $50 \Omega$ ) can be placed in series with the output to eliminate most peaking. The gain resistor $\left(\mathrm{R}_{\mathrm{G}}\right)$ can then be chosen to make up for any gain loss which may be created by this additional resistor at the output. In many cases it is also possible to simply increase the value of the feedback resistor $\left(\mathrm{R}_{\mathrm{F}}\right)$ to reduce the peaking.

## Current Limiting

The EL5392C has no internal current-limiting circuitry. If the output is shorted, it is possible to exceed the Absolute Maximum Rating for output current or power dissipation, potentially resulting in the destruction of the device.

## Power Dissipation

With the high output drive capability of the EL5392C, it is possible to exceed the $125^{\circ} \mathrm{C}$ Absolute Maximum junction temperature under certain very high load current conditions. Generally speaking when $\mathrm{R}_{\mathrm{L}}$ falls below about $25 \Omega$, it is important to calculate the maximum junction temperature ( $\mathrm{T}_{\text {JMAX }}$ ) for the application to determine if power supply voltages, load conditions, or package type need to be modified for the EL5392C to
remain in the safe operating area. These parameters are calculated as follows:

$$
\mathrm{T}_{\mathrm{JMAX}}=\mathrm{T}_{\mathrm{MAX}}+\left(\theta_{\mathrm{JA}} \times \mathrm{n} \times \mathrm{PD}_{\mathrm{MAX}}\right)
$$

where:
$\mathrm{T}_{\mathrm{MAX}}=$ Maximum Ambient Temperature
$\theta_{\mathrm{JA}}=$ Thermal Resistance of the Package
$\mathrm{n}=$ Number of Amplifiers in the Package
$\mathrm{PD}_{\mathrm{MAX}}=$ Maximum Power Dissipation of Each
Amplifier in the Package
PD ${ }_{\text {MAX }}$ for each amplifier can be calculated as follows:
$\mathrm{PD}_{\text {MAX }}=\left(2 \times \mathrm{V}_{\mathrm{S}} \times \mathrm{I}_{\text {SMAX }}\right)+\left[\left(\mathrm{V}_{\mathrm{S}}-\mathrm{V}_{\text {OUTMAX }}\right) \times \frac{\mathrm{V}_{\text {OUTMAX }}}{\mathrm{R}_{\mathrm{L}}}\right]$
where:
VS $=$ Supply Voltage
$I_{\text {Smax }}=$ Maximum Supply Current of 1 A
VOUTMAX $=$ Maximum Output Voltage (Required)
$\mathrm{R}_{\mathrm{L}}=$ Load Resistance

## General Disclaimer

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